

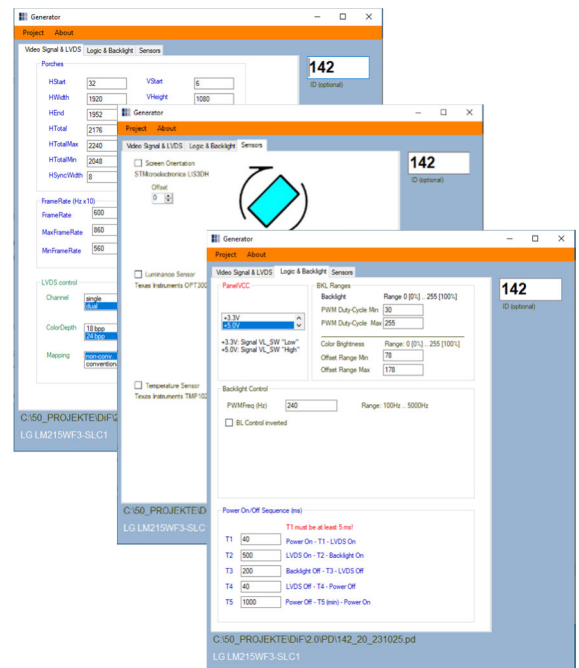
Components

The firmware of the d.scale-HDIII family consists of a basic firmware and the display-specific panel data block. Together with the required EDID files, these two blocks form the overall firmware. The following modules are available for generation, merging and programming

Generator

The generator creates the panel data block from the display-specific parameters. The following areas can be customized:

- Display resolution/timing
 - Single-/dual-channel LVDS
 - Color depth
 - Data-mapping
 - Conventional (Open-LDI)
 - Non-conventional (VEAS-/ TI-Mode)
 - Spread-spectrum
 - Drive-Strength
- Display control
 - Power-on/off sequencing
 - Supply-voltage control
 - Color Brightness
 - Backlight/LED-driver control
 - PWM-Frequency
 - PWM-Range
 - PWM standard/inverted
- Sensor Support/Control via DDC/CI – MCCS
 - Screen orientation (Pivot)
 - Luminance
 - Temperature



Composer

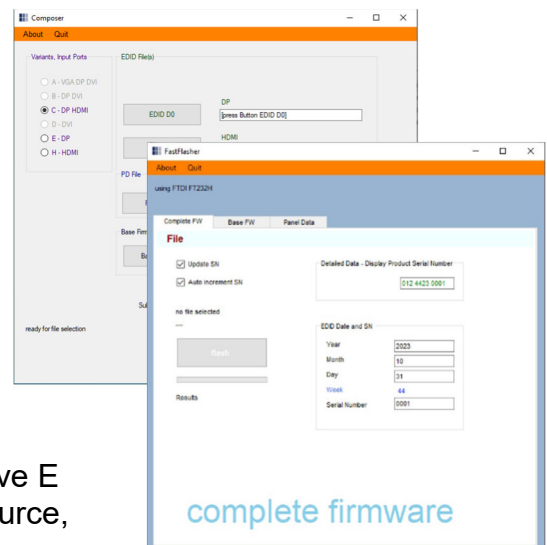
The Composer generates a new, complete firmware using the panel data created with the generator and the EDID files defined/specified by the user.

Programmer

The programmer enables the following programming options depending on requirements.

- Complete firmware
- Basic firmware only
- Panel data block only

In addition, the programmer takes over the standard compliant programming of the S/N number in the respective EDID files. The required programming hardware is open source, readily available and very inexpensive



complete firmware